Amendment to the Specification

Please amend the first full paragraph on page 7 of the application as originally filed as shown below. The paragraph shown below and amended corresponds to paragraph [0026] as published. No new matter has been added.

With reference now to Figure 2, a high level block diagram of a prior art receiver is depicted. The RX waveform 15 is received by the receiver and applied to the serial-to-parallel converter 22 where (continuing the above example) the serial stream is converted into 39 parallel streams and the 34 samples of guard time are removed from each frame. As is known in the art, OFDM frame synchronization allows for appropriate start and end locations for the frame. The output of the converter 22 is applied to the demodulator 24 where, for each frame, a Fast Fourier Transform ("FFT") is performed on the remaining 128 samples in the frame to detect the 39 tones. Either estimated symbols or estimated bits are output from the demodulator ~~modulator~~ 24 and applied to the parallel-to-serial converter 26 where the estimated symbols/bits are serialized and output as the signal 27.